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Substitute for form 1449B/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Date Submitted: September 24, 2003

(use as many sheets as necessary)

Complete if Known

Application Number	Unassigned
Filing Date	09/24/2003
First Named Inventor	Takashi SHIMIZU
Group Art Unit	Unassigned
Examiner Name	Unassigned
Attorney Docket Number	065686-0158

Sheet	1	of	1
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U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
MSJ	A3	G. SCOTT et al., "NMOS Drive Current Reduction Caused by Transistor Layout and Trench Isolation Induced Stress," IEDM Technical Digest, U.S.A., IEEE (1999) pp. 827-830.	

**Examiner
Signature**

Mary C. Jacob

Date
Considered

7/3/06

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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT Date Submitted: February 23, 2006 <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/668,974
				Filing Date	9/24/2003
				First Named Inventor	Takashi SHIMIZU
				Group Art Unit	2811
				Examiner Name	Unassigned
				Attorney Docket Number	065686-0158
Sheet	1	of	1		

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Examiner Signature	Mary C. H. Col-	Date Considered	7/13/06
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